

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: :
Naoki YADA et al. :
Application No.: Not Yet Assigned :
Filed: Herewith :
For: DATA PROCESSING SYSTEM AND :
DATE PROCESSING METHOD :

McLean, Virginia
August 27, 2001



#2

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The documents on the attached List are cited in the specification, on pages 2-4, and their relevance is addressed therein.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees that may be required by this paper, and to credit any overpayment to that account.

Respectfully submitted,
MILES & STOCKBRIDGE P.C.

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(Use several sheets if necessary)

XA-9538

Naoki YADA et al.

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		CA	Ikehashi et al., "A 60ns Access 32kByte 3-Transistor Flash for Low Power Embedded Applications," 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp 162-165.

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.